



### Product Summary

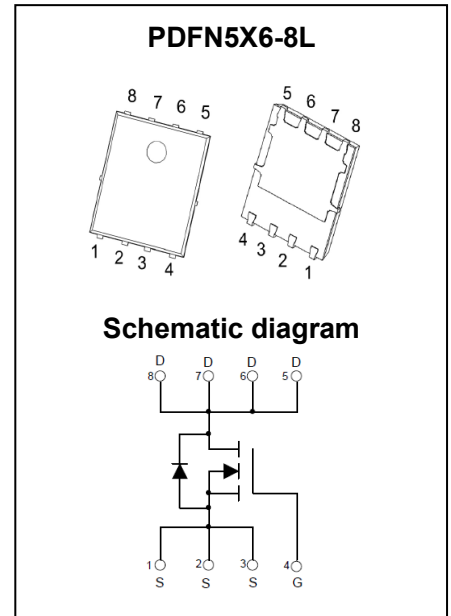
$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
40V	0.8m $\Omega$ @10V	300A

### Feature

- Split Gate Trench Technology
- Low  $R_{DS(on)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

### Application

- Power Management
- Load Switching



### Package Marking and Ordering Information

Part Number	Package	Marking	Packing	Reel Size	Tape Width	Qty
GPT009N04NNCU	PDFN5X6-8L	T009N04N	Reel & Tape	330mm	12mm	5000pcs

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$I_D$	$T_C = 25^\circ\text{C}$	300
		$T_C = 100^\circ\text{C}$	190
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	1200	A
Single Pulsed Avalanche Current <sup>3</sup>	$I_{AS}$	50	A
Single Pulsed Avalanche Energy <sup>3</sup>	$E_{AS}$	625	mJ
Power Dissipation <sup>5</sup>	$P_D$	156	W
Thermal Resistance from Junction to Ambient <sup>6</sup>	$R_{\theta JA}$	40	$^\circ\text{C/W}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	0.8	$^\circ\text{C/W}$
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55~ +150	$^\circ\text{C}$

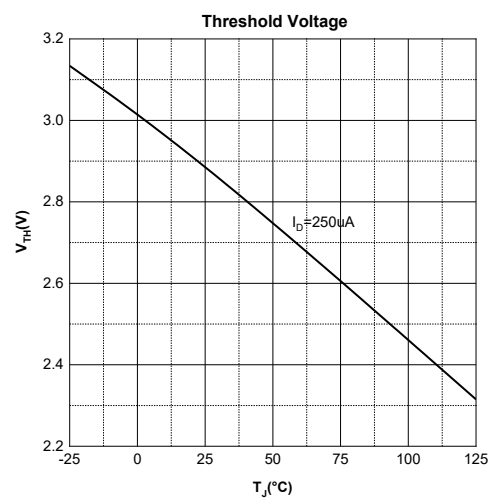
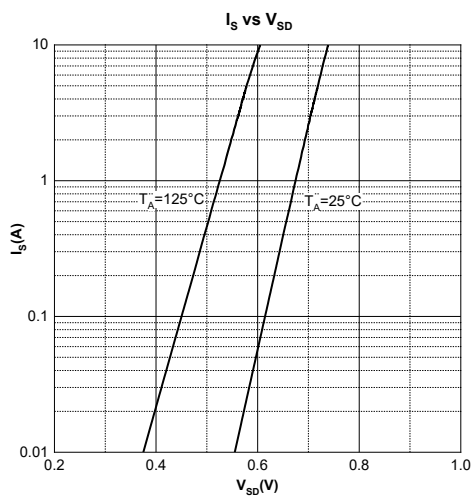
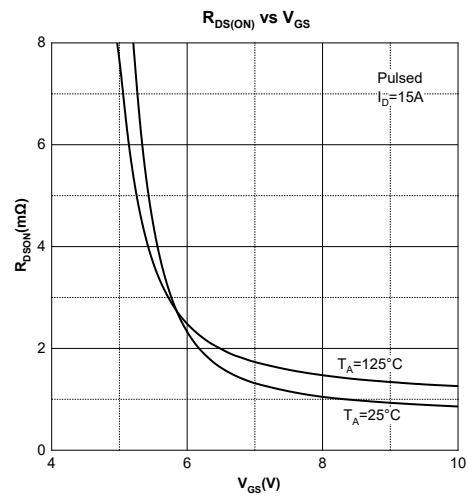
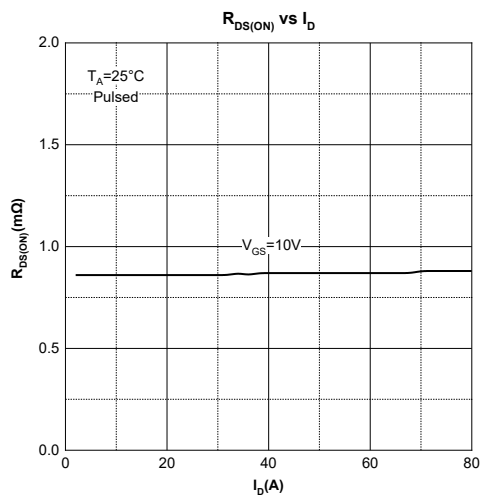
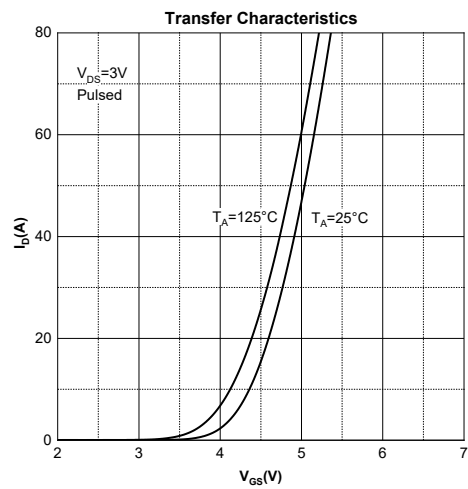
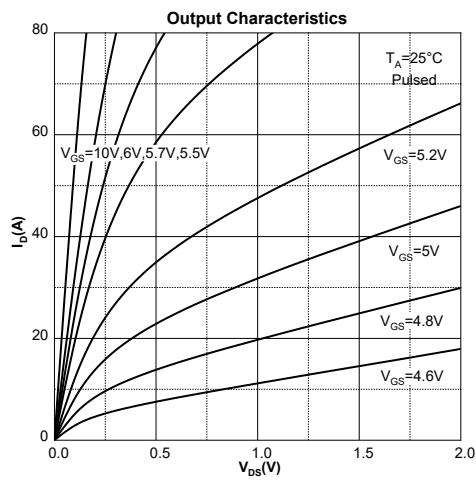
## MOSFET ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

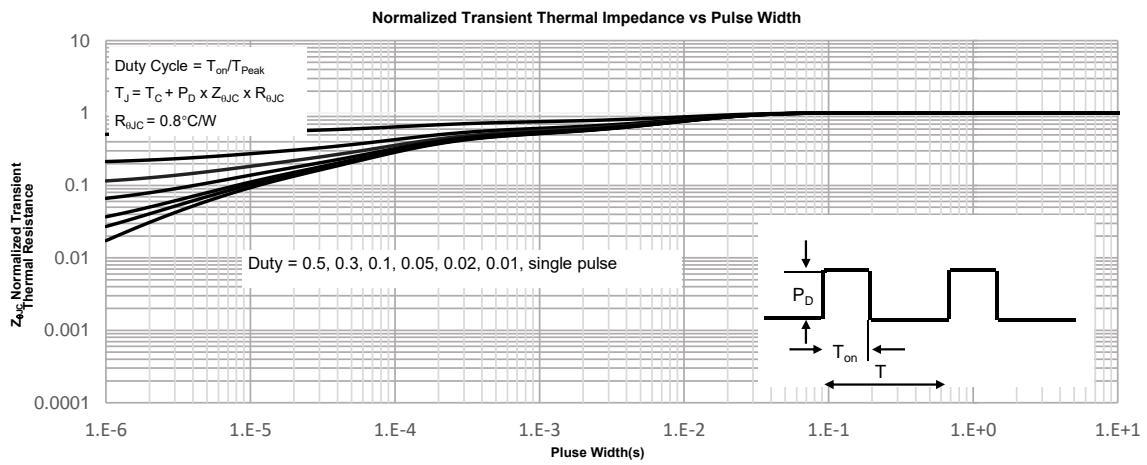
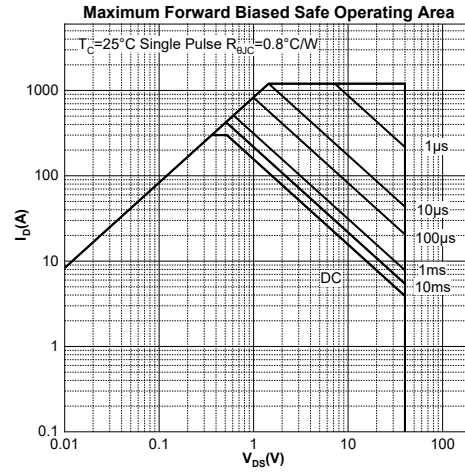
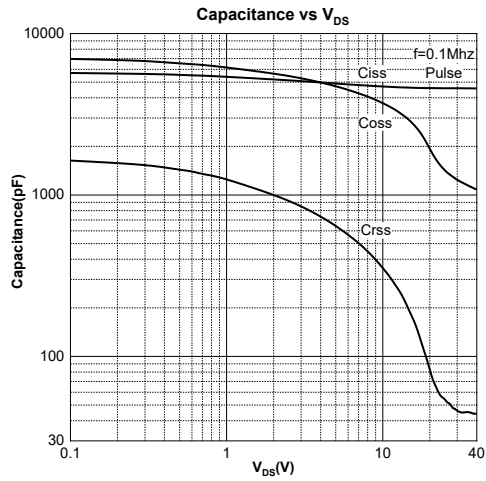
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	40			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V			1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
<b>On Characteristics<sup>4</sup></b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2	2.9	4	V
Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 15A		0.8	1.0	mΩ
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V, f = 0.1MHz		4575		pF
Output Capacitance	C <sub>oss</sub>			1916		
Reverse Transfer Capacitance	C <sub>rss</sub>			73		
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz		1.9		Ω
<b>Switching Characteristics</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A		72		nC
Gate-Source Charge	Q <sub>gs</sub>			19.6		
Gate-Drain Charge	Q <sub>gd</sub>			17		
Gate Plateau Voltage	V <sub>plateau</sub>			4.5		V
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> = 20V, V <sub>GS</sub> = 10V, R <sub>G</sub> = 2.7Ω, I <sub>D</sub> = 20A		20		ns
Turn-On Rise Time	t <sub>r</sub>			30		
Turn-Off Delay Time	t <sub>d(off)</sub>			44		
Turn-Off Fall Time	t <sub>f</sub>			21		
<b>Source-Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 15A			1.2	V
Diode Continuous Forward Current <sup>1</sup>	I <sub>S</sub>	T <sub>C</sub> = 25°C			300	A
Diode Pulse Forward Current <sup>2</sup>	I <sub>SM</sub>				1200	A
Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 20A, di/dt = 100A/μs		68		ns
Diode Reverse Recovery Charge	Q <sub>rr</sub>				110	

### Notes:

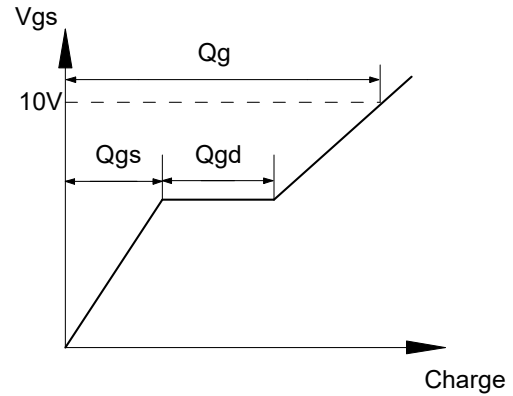
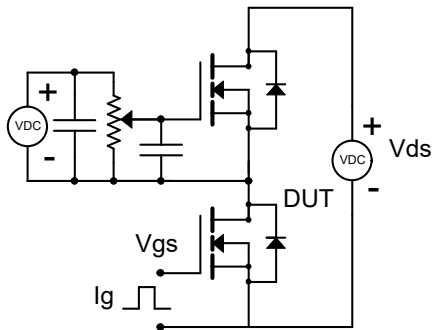
1. The maximum current rating is limited by package. And device mounted on a large heatsink.
2. Pulse Test: Pulse Width ≤ 10μs, duty cycle ≤ 1%.
3. E<sub>AS</sub> condition: V<sub>DD</sub> = 20V, V<sub>GS</sub> = 10V, L = 0.5mH, R<sub>G</sub> = 25Ω Starting T<sub>J</sub> = 25°C.
4. Pulse Test: Pulse Width ≤ 300μs, duty cycle ≤ 2%.
5. The power dissipation P<sub>D</sub> is limited by T<sub>J(MAX)</sub> = 150°C. And device mounted on a large heatsink.
6. Device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with T<sub>A</sub> = 25°C.

## Typical Characteristics

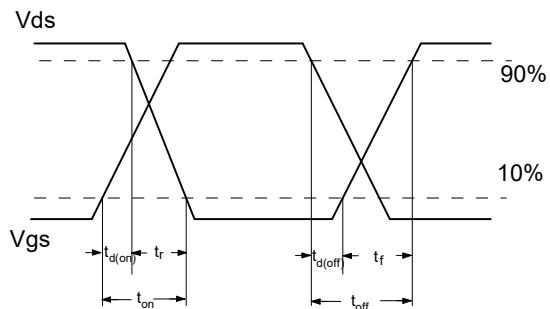
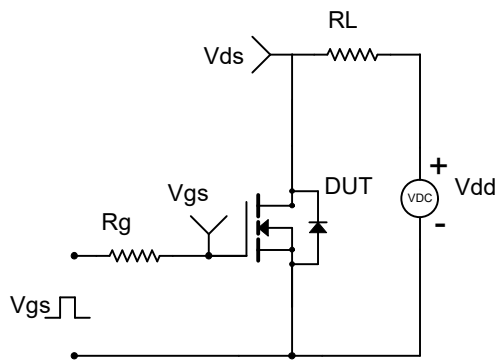




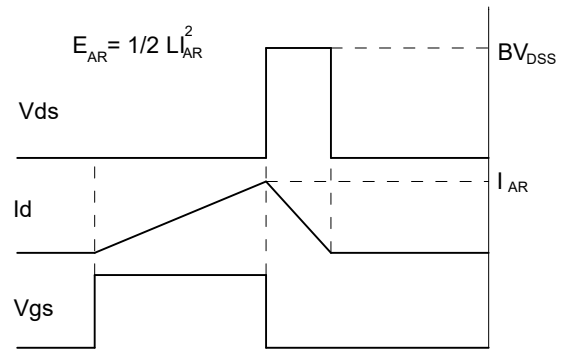
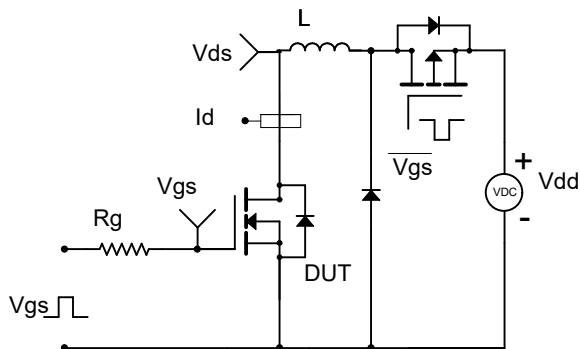
Gate Charge Test Circuit & Waveform



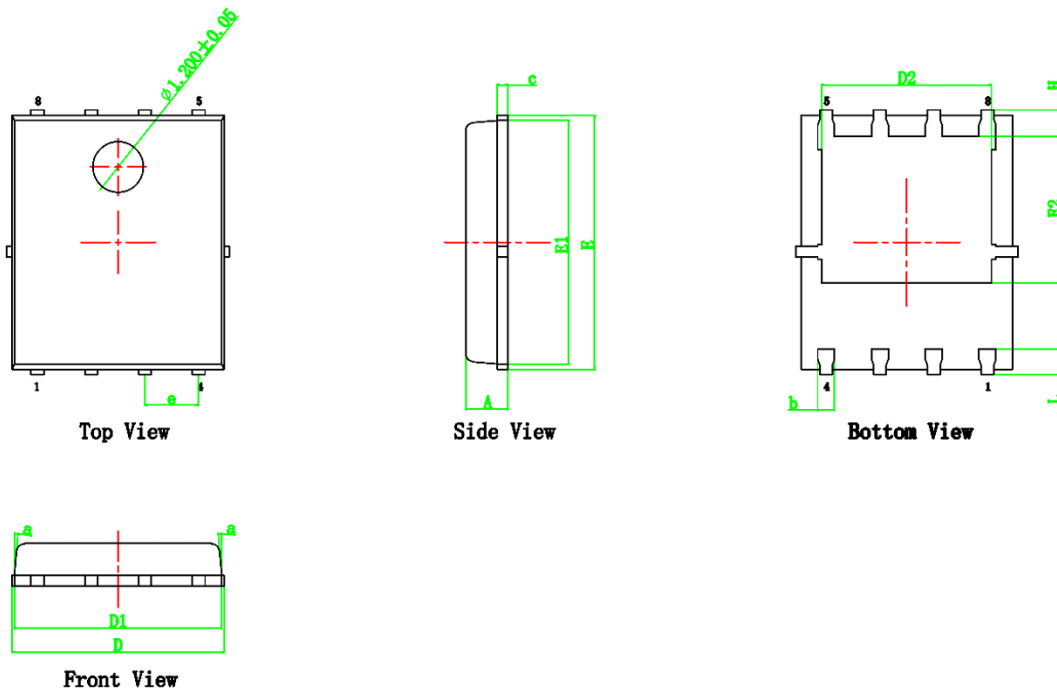
Resistive Switching Test Circuit & Waveform



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



## PDFN5X6-8L Package Information



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.200	0.035	0.047
b	0.330	0.510	0.013	0.020
c	0.190	0.300	0.007	0.012
D	4.800	5.220	0.189	0.210
D2	3.900	4.300	0.154	0.170
E	5.900	6.100	0.232	0.240
E1	5.700	5.800	0.224	0.228
E2	3.350	3.750	0.132	0.148
e	1.270REF		0.050REF	
H	0.350	0.720	0.014	0.028
D1	4.800	5.000	0.189	0.197
L	0.350	0.750	0.014	0.030
a	0°	12°	0°	12°

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- GreenPower Electronics reserves the right to improve product design function and reliability without notice.
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